



ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

## U.S. UTILITY PATENT APPLICATION

<p><b>O.I.P.E.</b></p> <p>SCANNED <u>HKm<sup>②</sup></u> Q.A. <u>LR</u></p>	<p><b>PATENT DATE</b></p>
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/800760	F	361	<del>088,899</del>	2836	<del>1816</del>

## APPLICANTS

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91.1

Jackson

**TITLE**

D.C. power supply circuit and electronic apparatus using such circuits

PTO-2040  
12/99

**PREPARED AND APPROVED FOR ISSUE**

## ISSUING CLASSIFICATION

ORIGINAL					CROSS REFERENCE(S)							
CLASS		SUBCLASS			CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)						
INTERNATIONAL CLASSIFICATION												
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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> a) The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> b) The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____    	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
	_____ (Primary Examiner) (Date)		<b>ISSUE FEE</b>	
			Amount Due	Date Paid
<input type="checkbox"/> c) The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		<b>ISSUE BATCH NUMBER</b>	
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Form **PTO-436A**  
(Rev. 10/97)

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